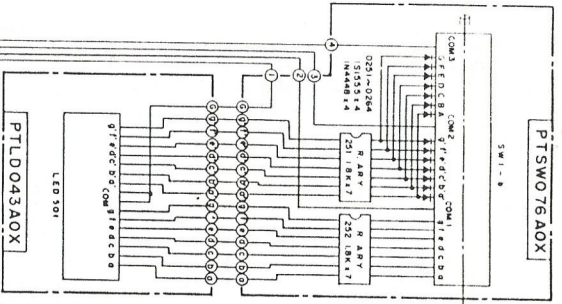
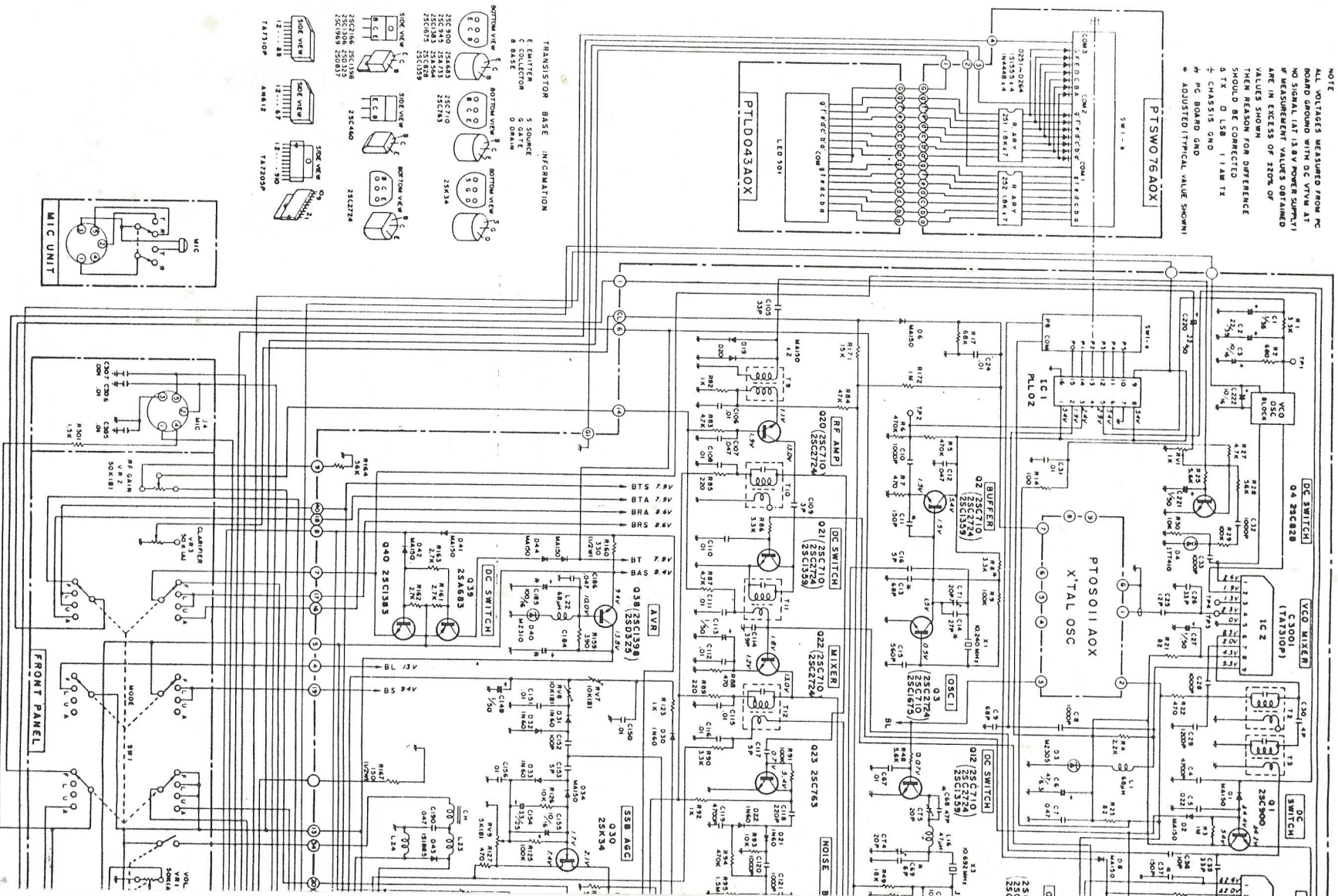
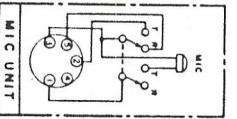
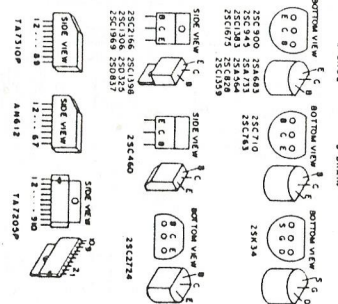


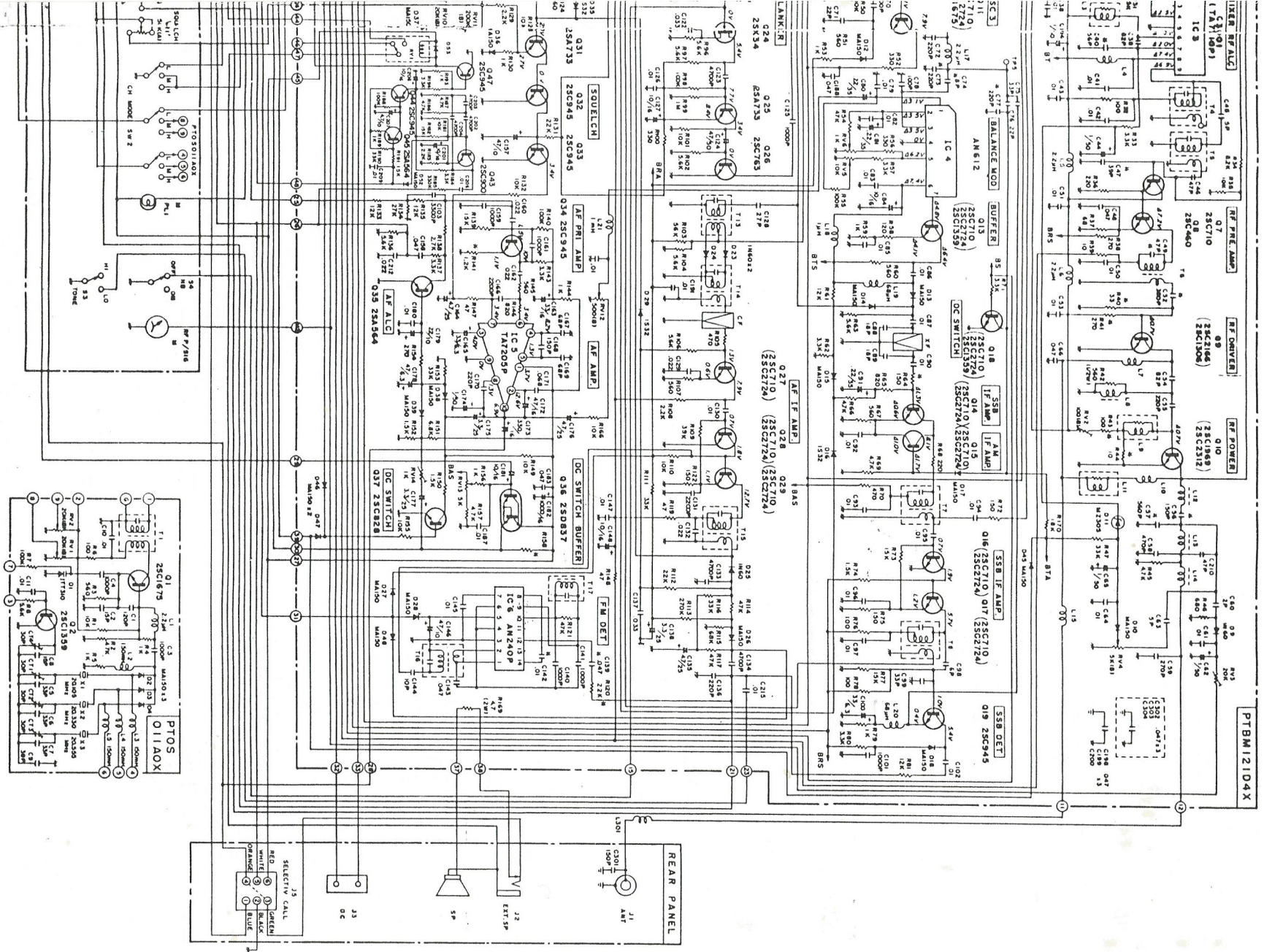
Schematic diagram

NOTE
 ALL VOLTAGES MEASURED FROM PC BOARD GROUND WITH DC VTM AT NO SIGNAL (AT 1.5 BV POWER SUPPLY) * MEASUREMENT VALUES OBTAINED ARE IN EXCESS OF 20DB OF VALUES SHOWN THEN REASON FOR DIFFERENCE SHOULD BE CORRECTED
 † TX Q LSR 1.1 AM TX
 ‡ CHASSIS GND
 § PC BOARD GND
 * ADJUSTED TYPICAL VALUE SHOWN



TRANSISTOR BASE INFORMATION
 C EMITTER
 E SOURCE
 B BASE
 O DRAIN





PTBM12104X

